

## AMENDMENTS

### IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) A PCI bus interface circuit for the voltage supply of a PCI plug-in card that can be connected to a PCI bus, the PCI bus interface circuit comprising:

a first input for connection to a main voltage supply line of the PCI bus;

a second input for connection to an auxiliary voltage supply line of the PCI bus;

an output for outputting a supply voltage to the PCI plug-in card;

a first ~~switching device~~ transistor for switching a main supply voltage that is present at the first input to the output, the first transistor having a first control terminal connected to the second input so that the first transistor switches the main supply voltage that is present at the first input to the output if no auxiliary supply voltage  $V_{aux}$  is present at the second input;

a second ~~switching device~~ transistor for switching an auxiliary supply voltage  $V_{aux}$  that is present at the second input to the output, the second transistor having a second control terminal connected to the first input so that the second transistor switches the auxiliary supply voltage that is present at the second input to the output if no main supply voltage  $V_{cc}$  is present at the first input; and having

a third ~~switching device~~ transistor, which, given the simultaneous presence of a main supply voltage  $V_{cc}$  at the first input and an auxiliary supply voltage  $V_{aux}$  at the second input, drives the second ~~switching device~~ transistor for switching the auxiliary

supply voltage  $V_{aux}$  through to the output, the third transistor being constructed complementarily with respect to the first and second transistors and having a third control terminal connected to the second input so that the third transistor, when an auxiliary supply voltage is applied to the second input, turns on and connects the second control terminal to a specific voltage potential with the result that the second transistor switches through the auxiliary supply voltage to the output.

2. (Cancelled)

3. (Cancelled)

4. (Cancelled)

5. (Cancelled)

6. (Cancelled)

7. (Previously Presented) The interface circuit as claimed in claim 1, wherein respective current limiting resistors are connected upstream of the control terminals of the first and second transistors.

8. (Currently Amended) The interface circuit as claimed in claim 1, wherein the switching point of the third ~~switching device~~ transistor is adjustable by means of a voltage divider.

9. (Previously Presented) The interface circuit as claimed in claim 1, wherein provision is made of a detection line, connected to the second input, for outputting the auxiliary supply voltage to a voltage detection device within the circuit situated on the plug-in card.

10. (Currently Amended) The interface circuit as claimed in claim 1, wherein the ~~switching devices~~ transistors have a small voltage drop in the turned-on state.

11. (Currently Amended) The interface circuit as claimed in claim 10, wherein the ~~switching devices~~ transistors have a voltage drop of less than 0.1 volt in the turned-on state.

12. (Currently Amended) The interface circuit as claimed in claim 1, wherein the main supply voltage and the ~~secondary~~ auxiliary supply voltage are in each case 3.1 volts to 3.5 volts.